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EFS ID:	1696785				
Application Number:	10072145				
International Application Number:					
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Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance				
First Named Inventor/Applicant Name:	Guy E. Averett				
Correspondence Address:	ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 - Phoenix AZ 85082-2890 US 6022443169 -				
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1	Amendment After Final	ONS00317AMENDMENT.pd f	302643	no	9		
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